

S/N 10/810,035

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner

Examiner: Thinh Nguyen

Serial No.: 10/810,035

Group Art Unit: 2818

Filed: March 26, 2004

Docket No.: 2800.450US1

Customer No.: 76287

Confirmation No.: 8458

Title: Non-volatile transistor memory array incorporating read-only elements with
single mask set

AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Office Action mailed October 16, 2008, please amend the application
as follows: